

PATENT NUMBER

U.S. **UTILITY** Patent Application

562
568-58

O.I.P.E.

PATENT DATE

SCANNED

Q.

APPLICATION NO. 09/615449	CONT/PRIOR F	CLASS 257	SUBCLASS 741 ⁺	ART UNIT 2811	EXAMINER K.A. ... D.T. ... S. CHANDRA
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APPLICANTS

Hideomi Suzawa
Koji Ono

Wiring and manufacturing method thereof, semiconductor device comprising said wiring, and dry etching method

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED <div> <div>Total Claims</div> <div>Print Claim for O.G.</div> </div>	
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <div> <div>_____ (Assistant Examiner)</div> <div>_____ (Date)</div> </div>		NOTICE OF ALLOWANCE MAILED <div> <div>_____</div> </div>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div> <div>_____</div> <div>_____</div> </div>	<div> <div>_____ (Primary Examiner)</div> <div>_____ (Date)</div> </div>		ISSUE FEE <div> <div>Amount Due</div> <div>Date Paid</div> </div>	
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